

FIG. 1

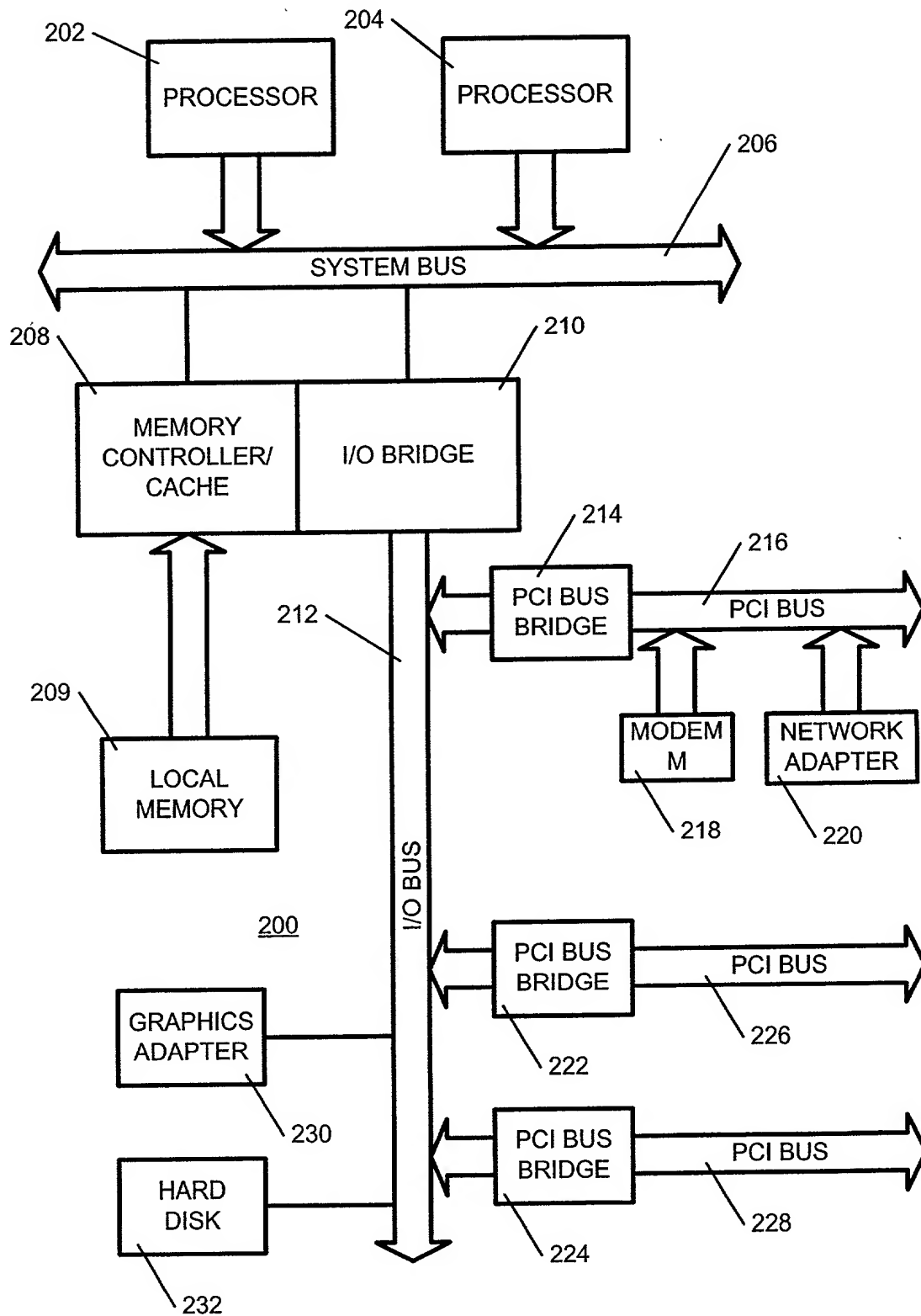


FIG. 2

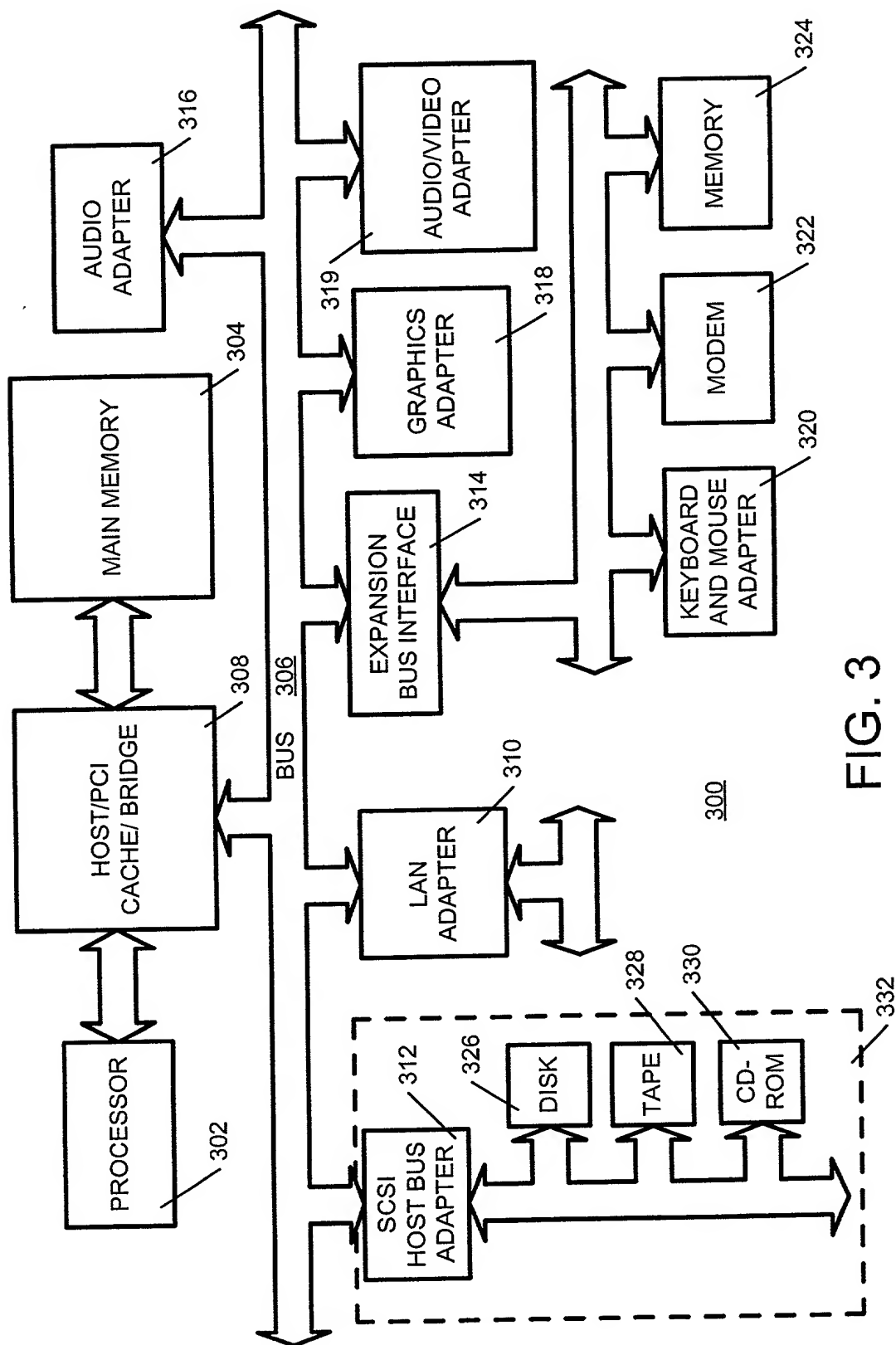


FIG. 3

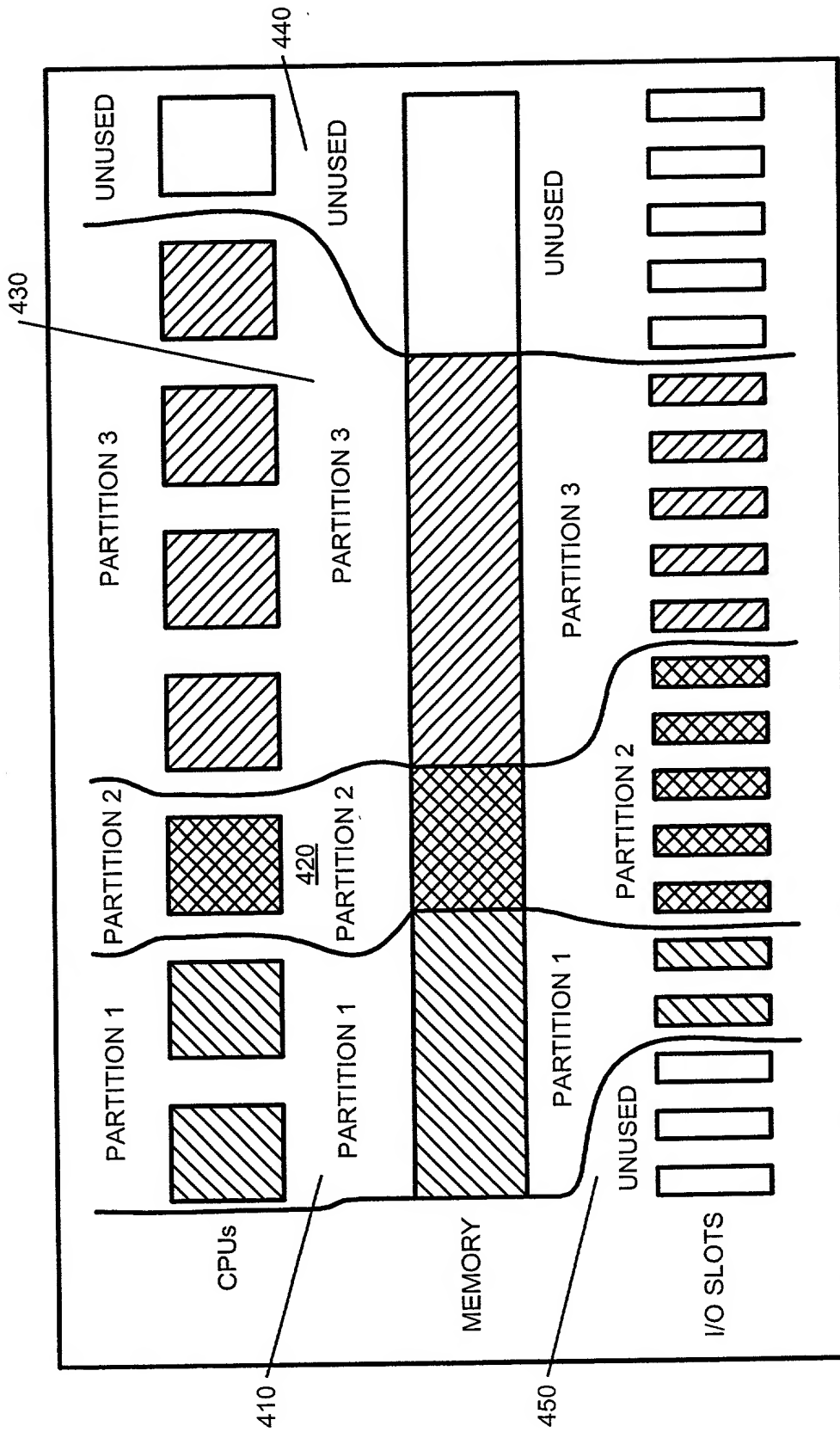


FIG. 4

	CPU'S	MEMORY LOCATIONS	I/O SLOTS
500 PARTITION <sub>1</sub>	CPU <sub>1</sub> & CPU <sub>2</sub>	M <sub>1</sub> - M <sub>50</sub>	SLOT <sub>4</sub> & SLOT <sub>5</sub>
502 PARTITION <sub>2</sub>	CPU <sub>3</sub>	M <sub>51</sub> - M <sub>75</sub>	SLOT <sub>6</sub> & SLOT <sub>10</sub>
504 PARTITION <sub>3</sub>	CPU <sub>4</sub> - CPU <sub>5</sub>	M <sub>76</sub> - M <sub>150</sub>	SLOT <sub>11</sub> & SLOT <sub>15</sub>

FIG. 5

	CPU'S	MEMORY LOCATIONS	I/O SLOTS
PARTITION <sub>1</sub>	CPU <sub>1</sub> & CPU <sub>2</sub>	M <sub>21</sub> - M <sub>50</sub>	SLOT <sub>4</sub> & SLOT <sub>5</sub>
PARTITION <sub>2</sub>	CPU <sub>3</sub>	M <sub>1</sub> - M <sub>20</sub> & M <sub>51</sub> - M <sub>75</sub>	SLOT <sub>6</sub> - SLOT <sub>10</sub>
PARTITION <sub>3</sub>	CPU <sub>4</sub> - CPU <sub>7</sub>	M <sub>76</sub> - M <sub>150</sub>	SLOT <sub>11</sub> - SLOT <sub>15</sub>

FIG. 6

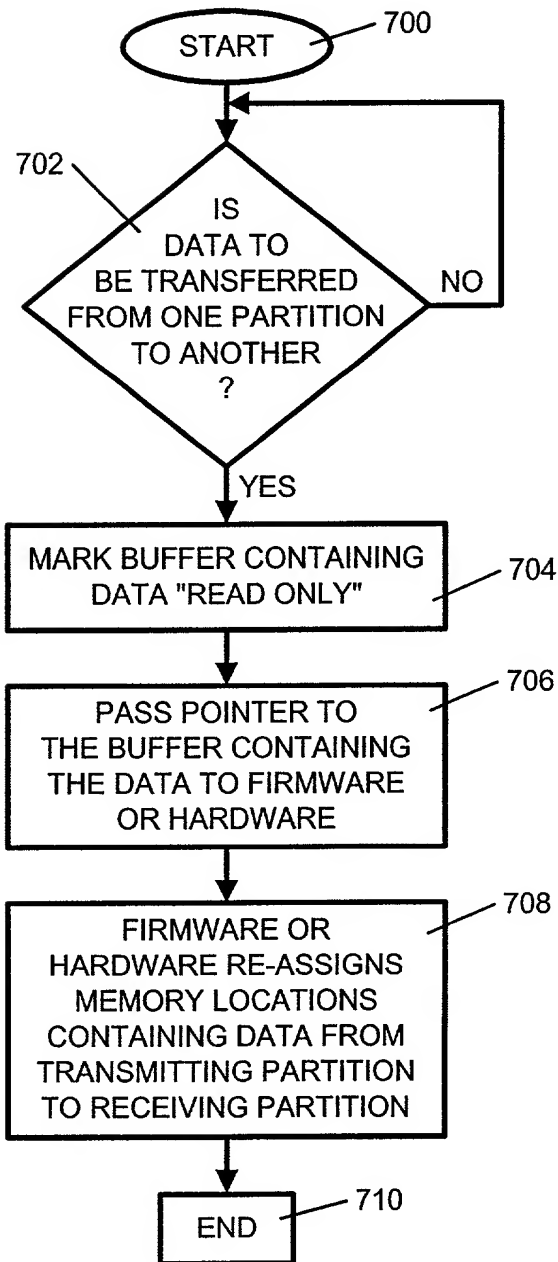


FIG. 7

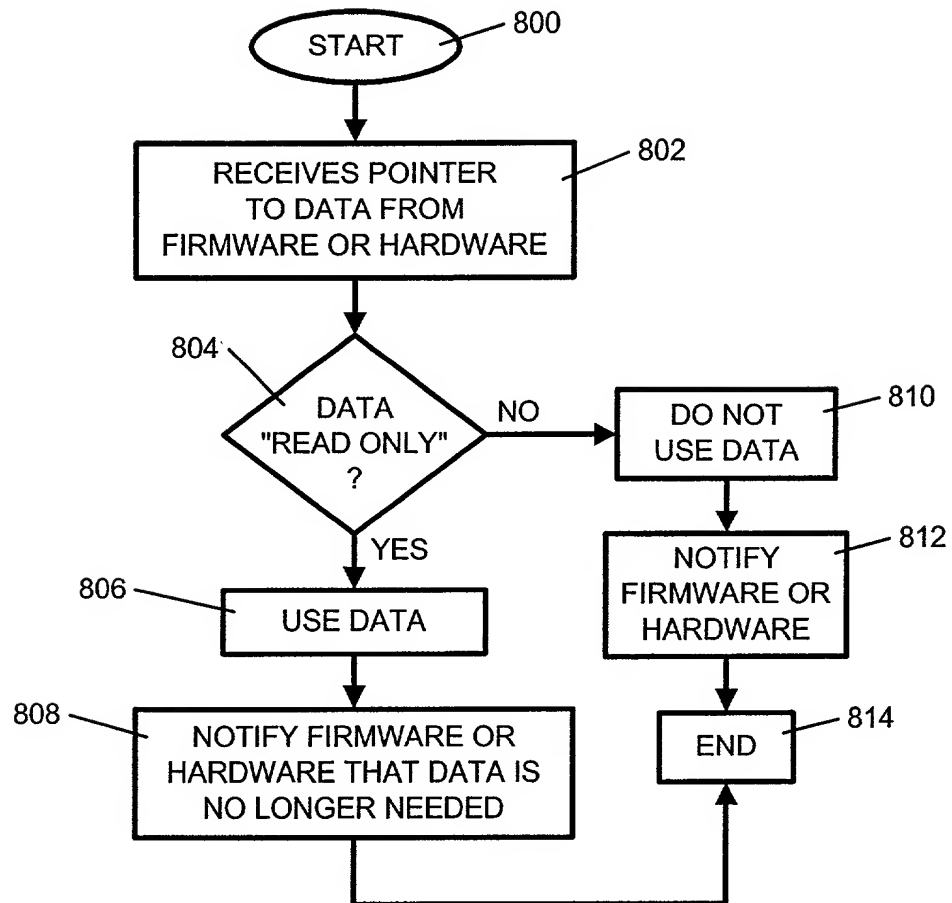


FIG. 8